

## **AMENDMENTS TO THE BRIEF DESCRIPTION OF THE DRAWINGS**

The following amendments are required by the inclusion of drawings in the specification that had been previously contained in the non-compliant appendix (part A). In accordance with 37 CFR 1.121(d), these amendments introduce no new matter into the disclosure of the application.

### **Brief Description of the Drawings**

FIG. 8 shows the structure of a typical engine program in NVCL.

FIG. 9 shows the Virtual Machine Register and Memory-Word Specifications.

FIG. 10 shows the Node Counter Register, which contains the node pointer.

FIG. 11 shows the two versions of the Index Word structure.

FIG. 12 shows the Index Word Register, which holds the contents of one index word.

FIG. 13 shows the Data-Definition Word and the Data-Definition Register.

FIG. 14 shows the four configurations of a Data Word.

FIG. 15 shows the architecture of the Data-Word Registers DWR-P & DWR-N.

FIG. 16 shows the Number of Nodes Register.

FIG. 17 shows the Data-Definition Count Register.

FIG. 18 shows the Node Free Index storage array structure.

FIG. 19 shows the structure of the Next Free Node Registers, NFNR 1 & NFNR 2.

FIG. 20 shows the architecture of the Field-Type Register and Field-Type Table.

FIG. 21 shows the architecture of the Index-segment Base-address Registers (IBR) and IBR Stack.

FIG. 22 shows the Arithmetic Stack Register.

FIG. 23 shows the Parameter Configuration Register.